

Parallel Computation

"Faster" is the only reason

But...

- greater programming complexity

- new kinds of bugs

 - ...and not much help for fixing them.

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Can the day or week of user effort be recovered?

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Can the day or week of user effort be recovered?

16384 core EPFL IBM BlueGene/P

1 hour at 850MHz

6 months at 3GHz

Parallel Computation

A simulation run takes about a second

want to do 1000's of them,

varying a dozen or so parameters.

A simulation run takes hours.

want to spread the problem over several machines.

Parallel Computation

A simulation run takes about a second

want to do 1000's of them,

varying a dozen or so parameters.

- Screensaver Calin-Jageman and Katz, 2006
- Bulletin-board (Linda)

A simulation run takes hours.

want to spread the problem over several machines.

Parallel Computation

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Network

Subnets on different machines

Cells communicate by:

logical spike events with significant
axonal, synaptic delay.

postsynaptic conductance depends
continuously on presynaptic voltage.

gap junctions

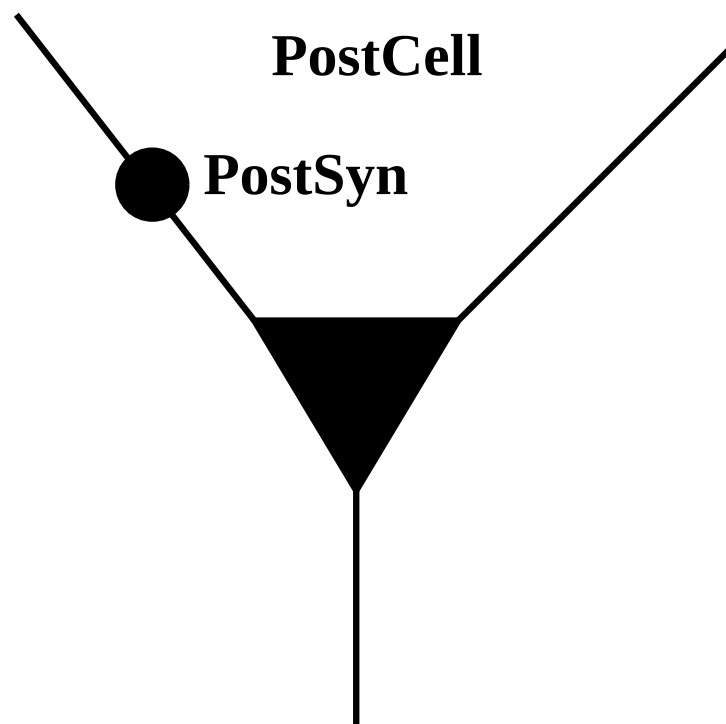
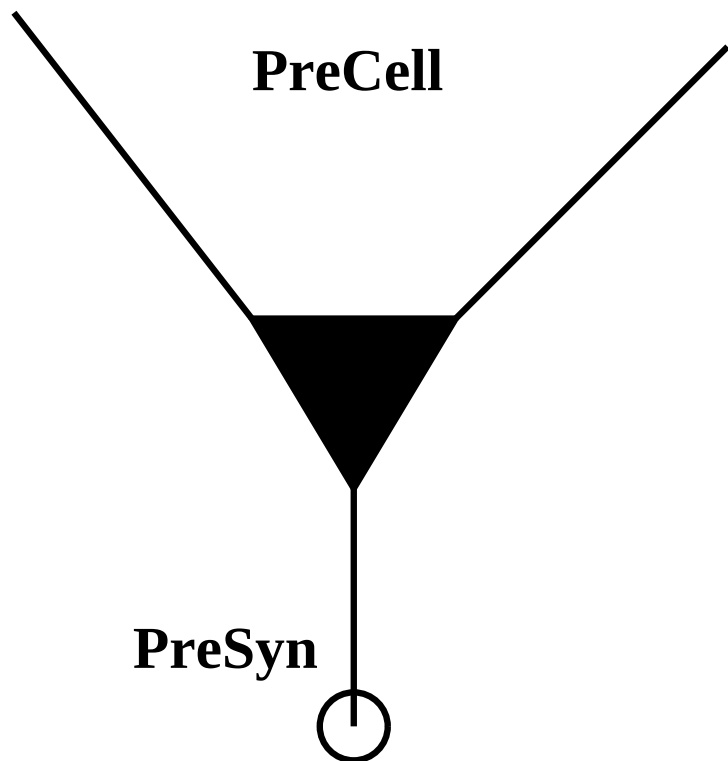
Parallel Computation

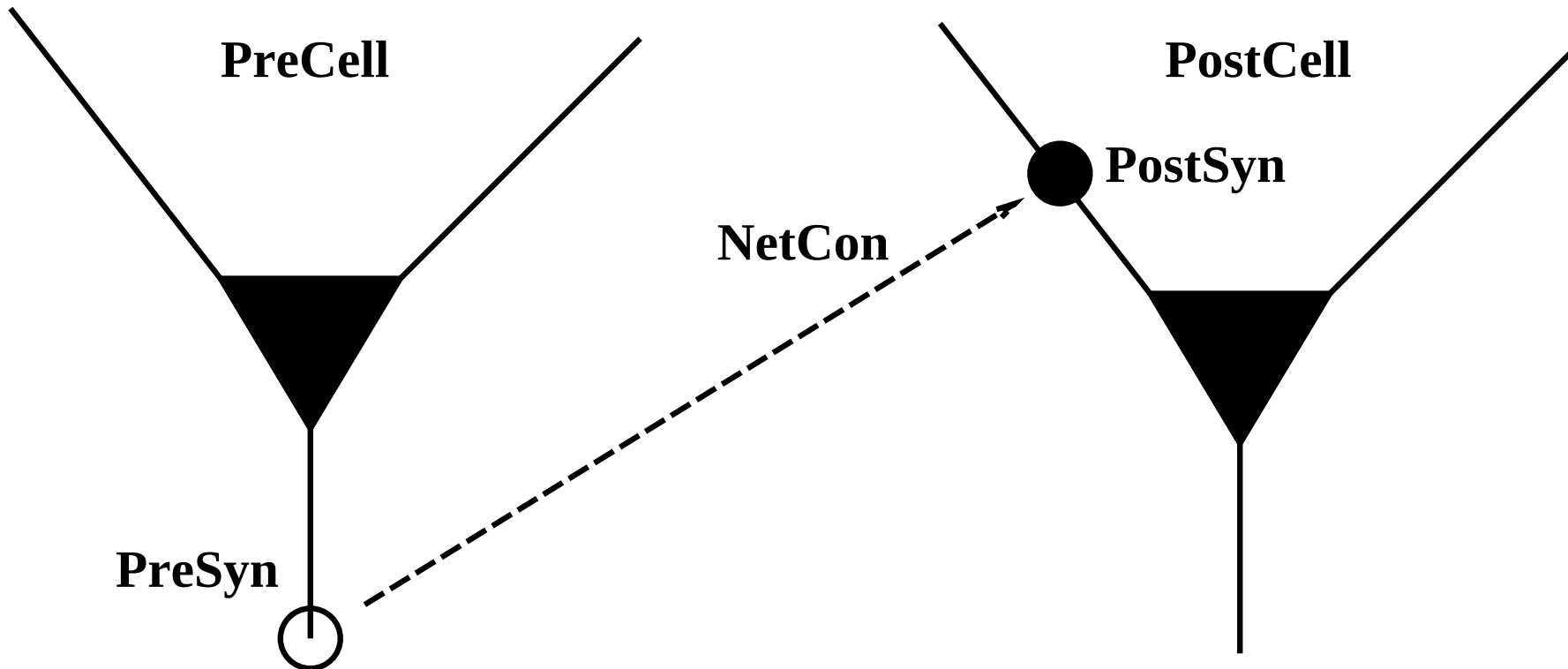
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want to spread the problem over several machines.

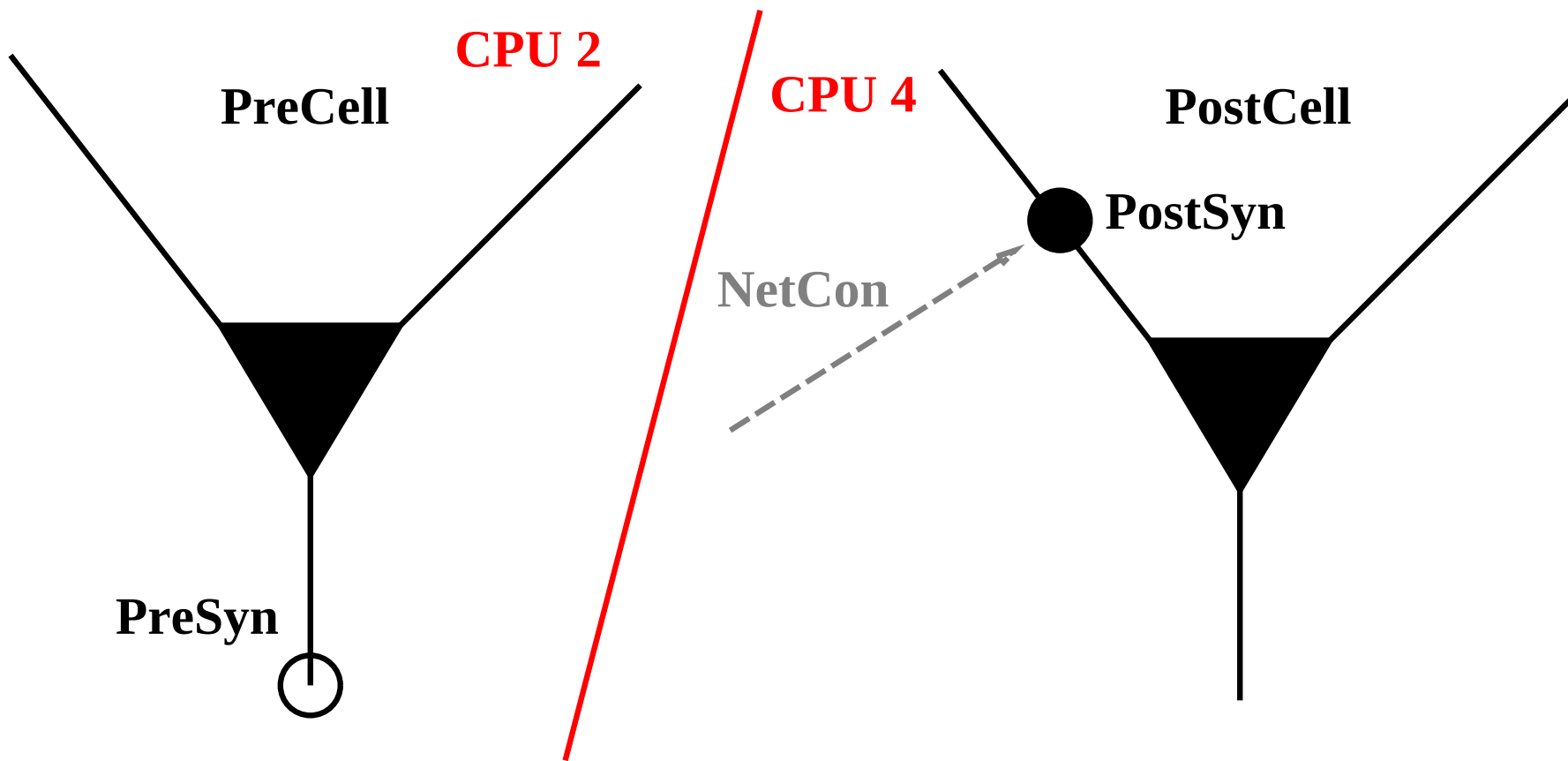
Single cells

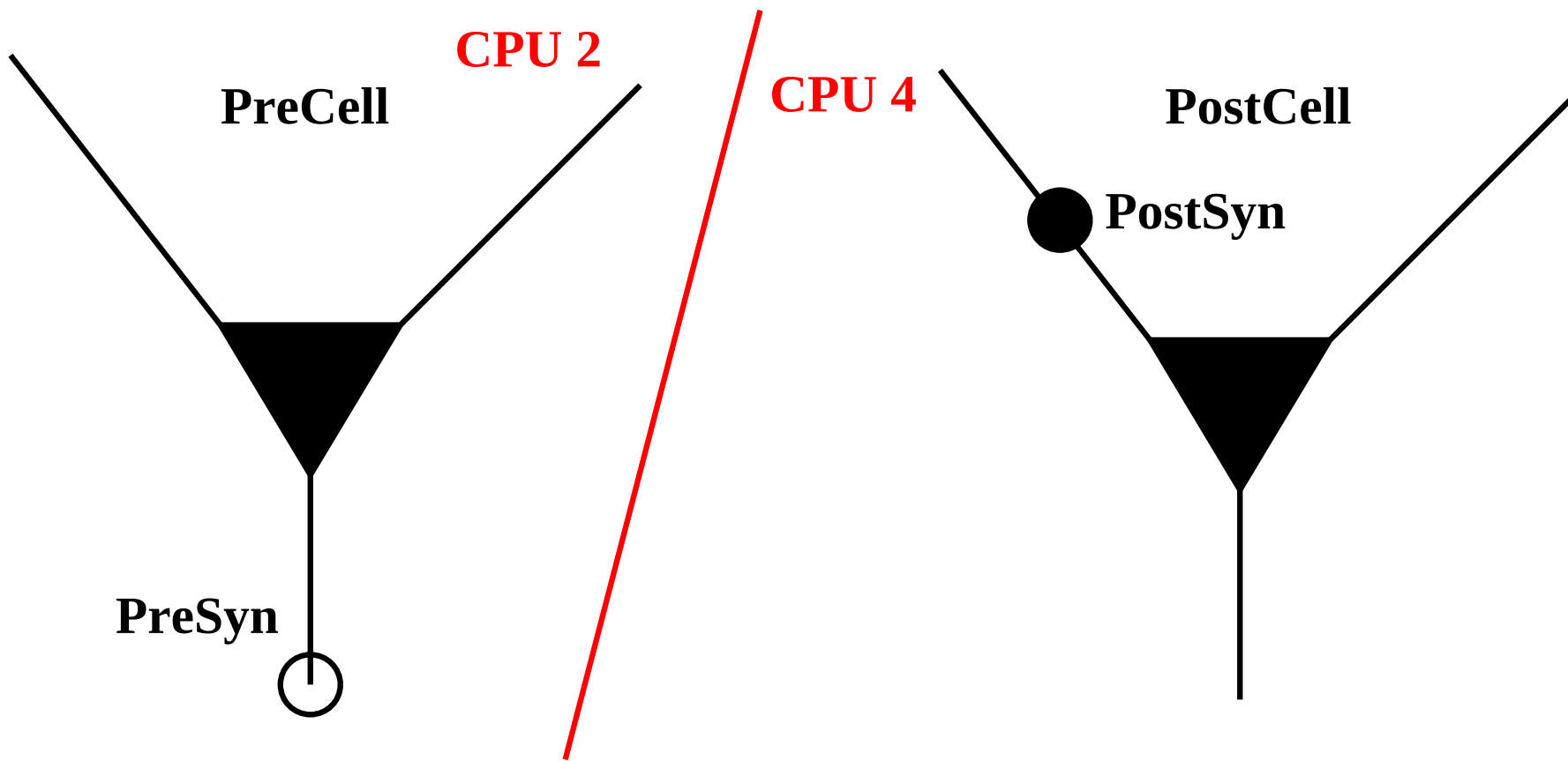
portions of the tree cable equation on
different machines.



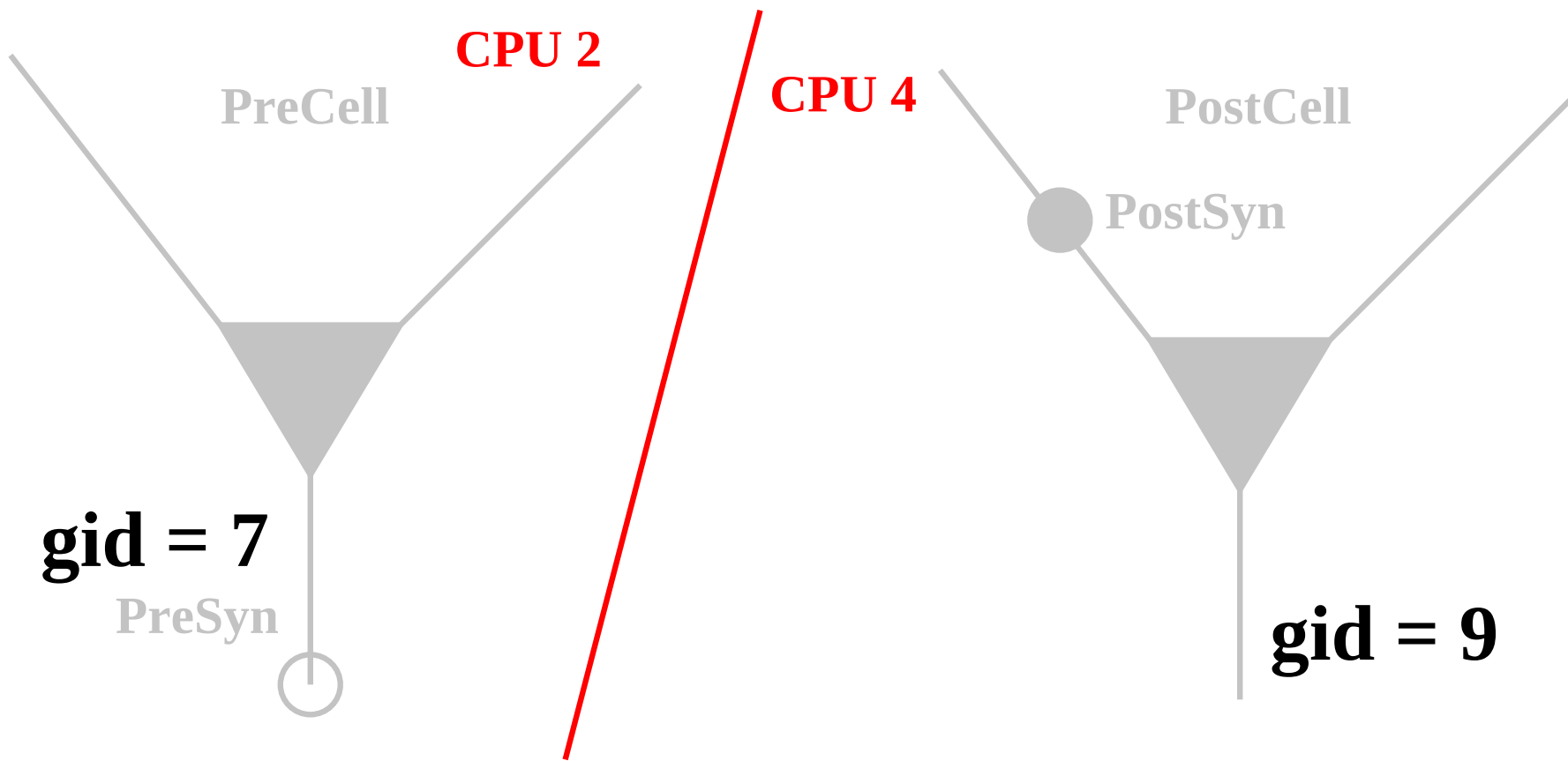


```
nc = new NetCon(PreSyn, PostSyn)
```





```
pc = new ParallelContext()
```



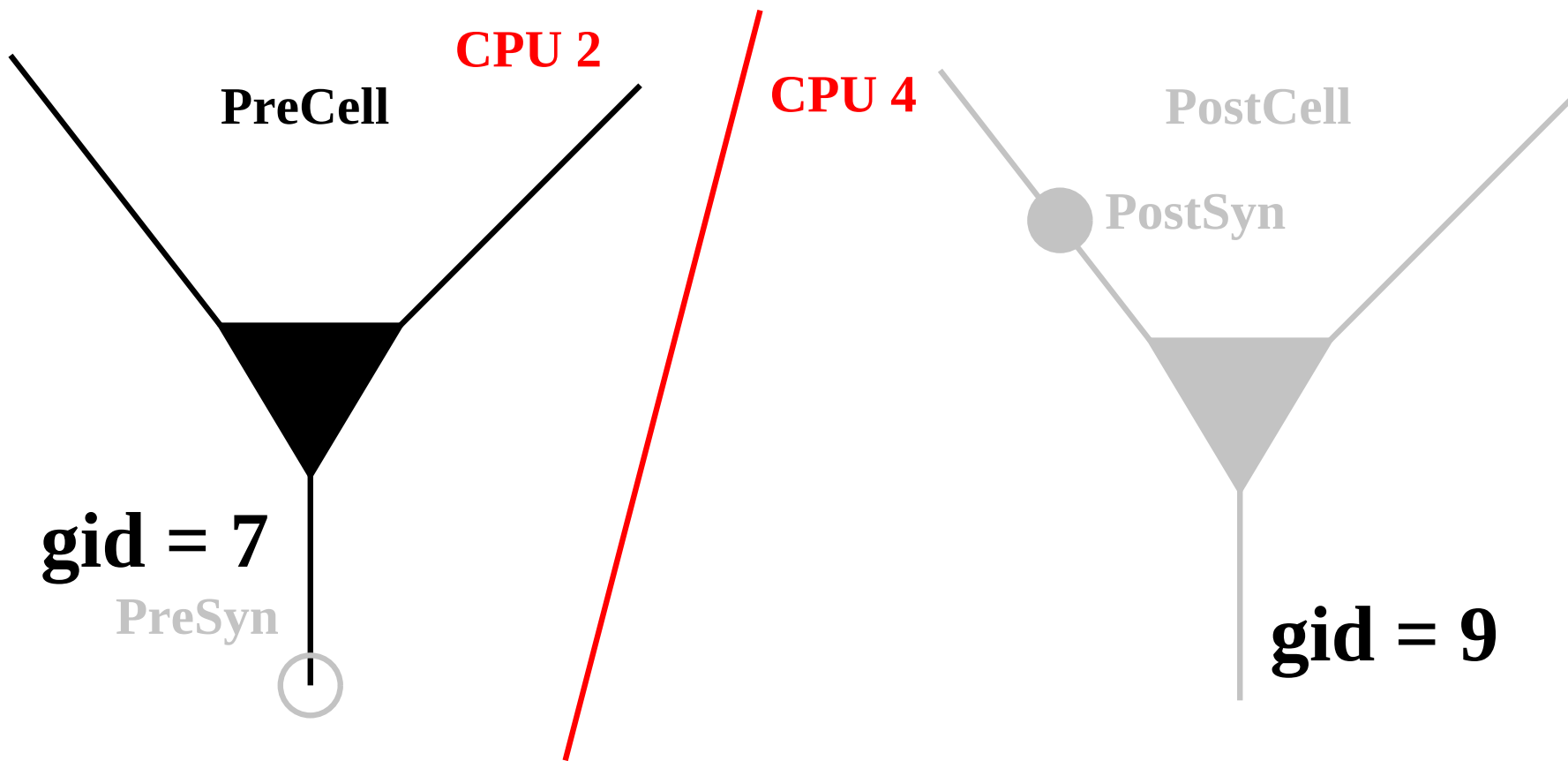
Every spike source (cell) must have a global id number.

CPU 0			CPU 3			CPU 4	
pc.id	0		pc.id	3		pc.id	4
pc.nhost	5		pc.nhost	5		pc.nhost	5
ncell	14	...	ncell	14		ncell	14
gid			gid			gid	
	0			3			4
	5			8			9
	10			13			

An efficient way to distribute:

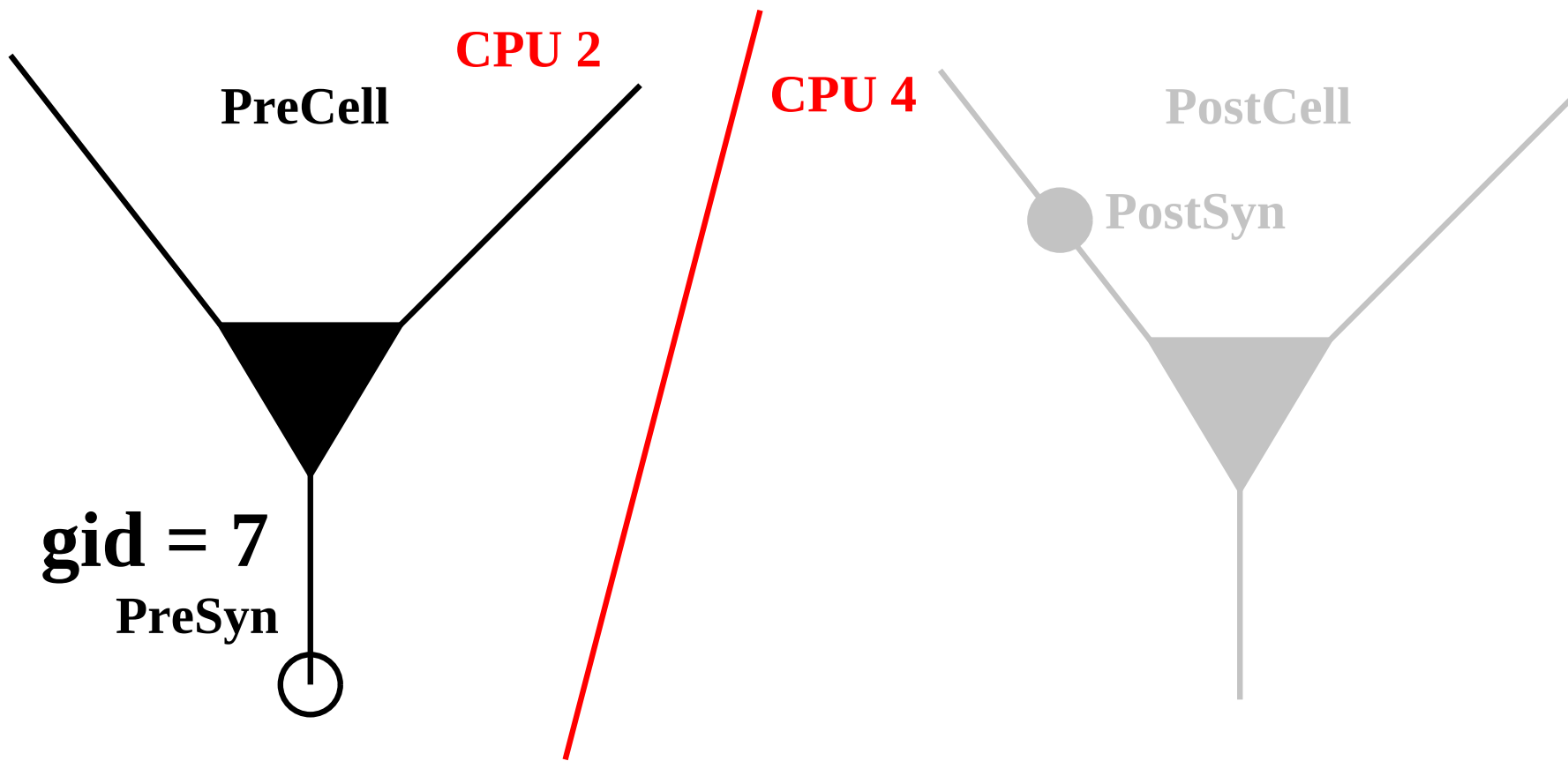
```
for (gid = pc.id; gid < ncell; gid += pc.nhost) {
    pc.set_gid2node(gid, pc.id)
    ...
}
```

body executed only ncell/nhost times, not ncell.



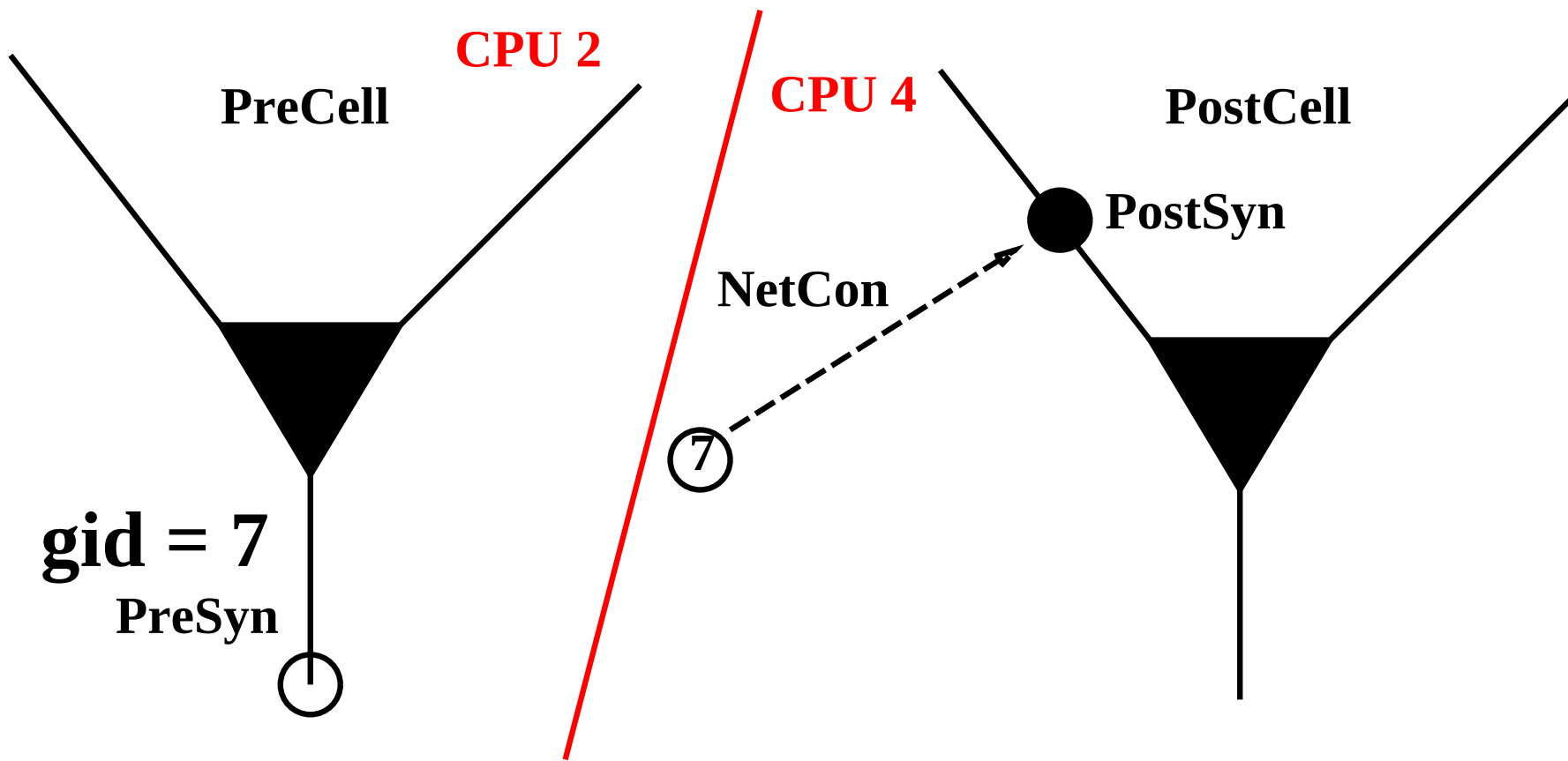
Create cell only where the gid exists.

```
if (pc.gid_exists(7)) {  
    PreCell = new Cell()  
}
```



Associate gid with spike source.

```
nc = new NetCon(PreSyn, nil)
pc.cell(7, nc)
```



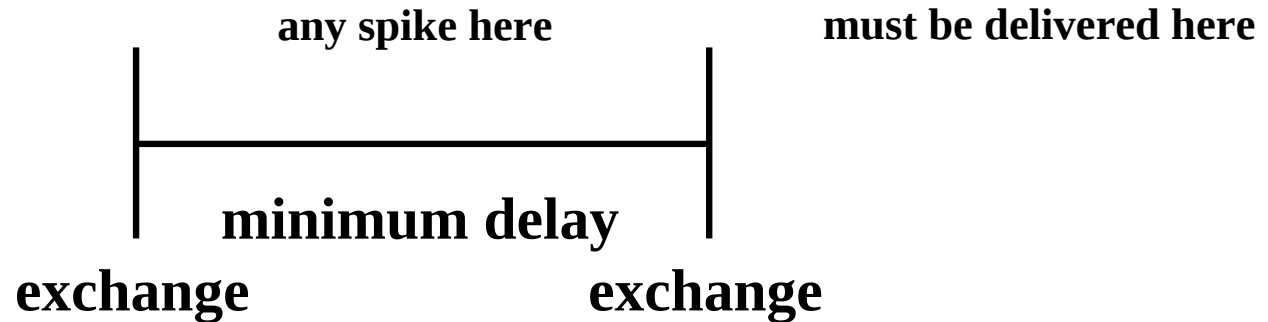
Create NetCon on CPU where target exists.

```
nc = pc.gid_connect(7, PostSyn)
```

Run using the idiom

```
pc.set_maxstep(10)  
stdinit()  
pc.solve(tstop)
```

pc.set_maxstep() uses
MPI_Allreduce
to determine minimum delay.



CPU 2

PreCell

CPU 4

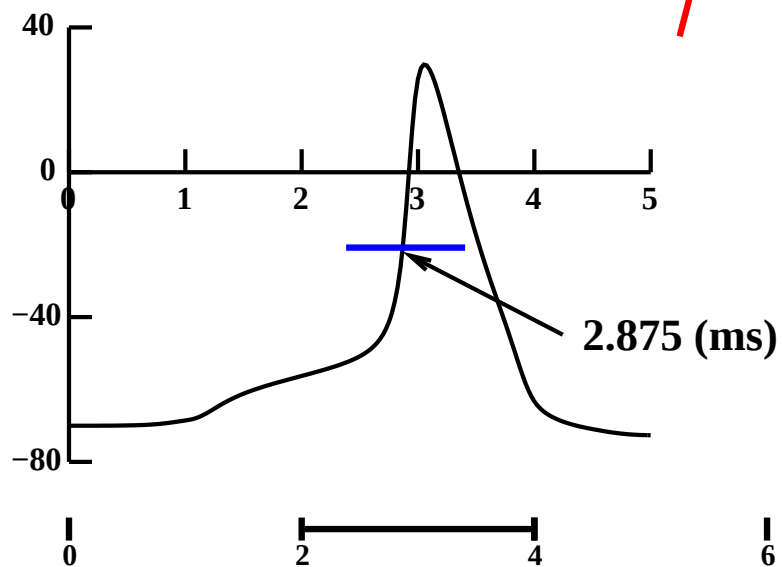
PostCell

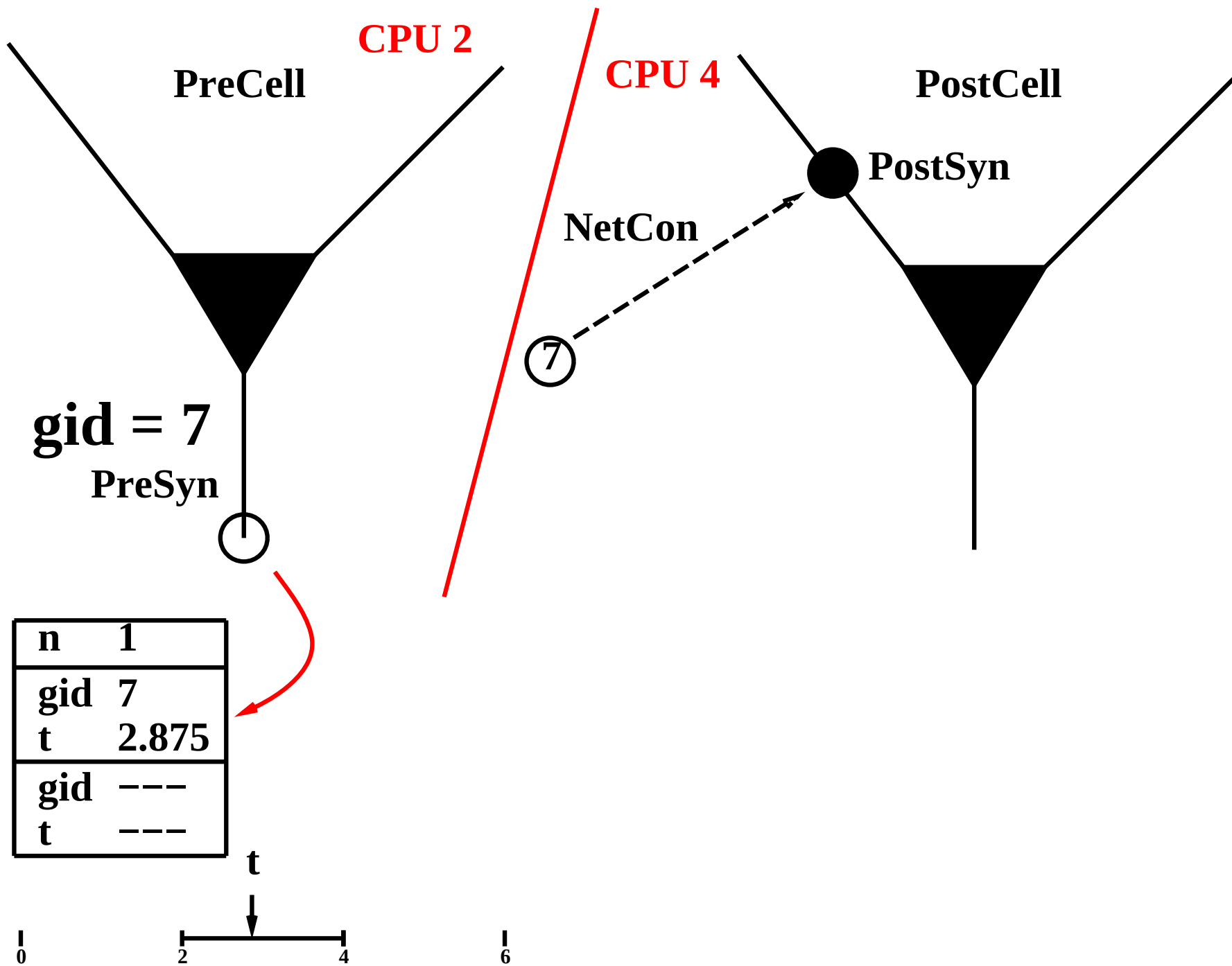
PostSyn

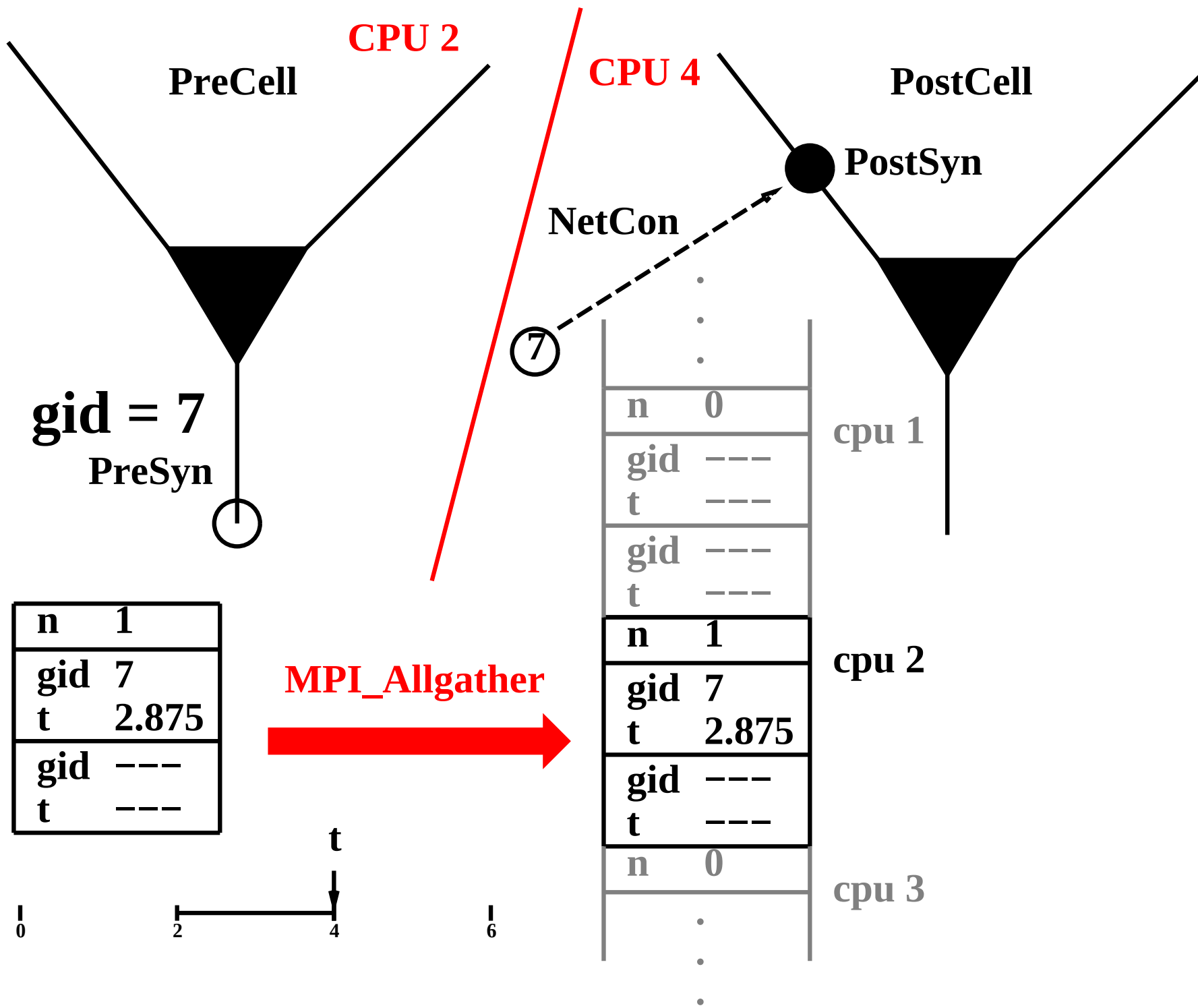
NetCon

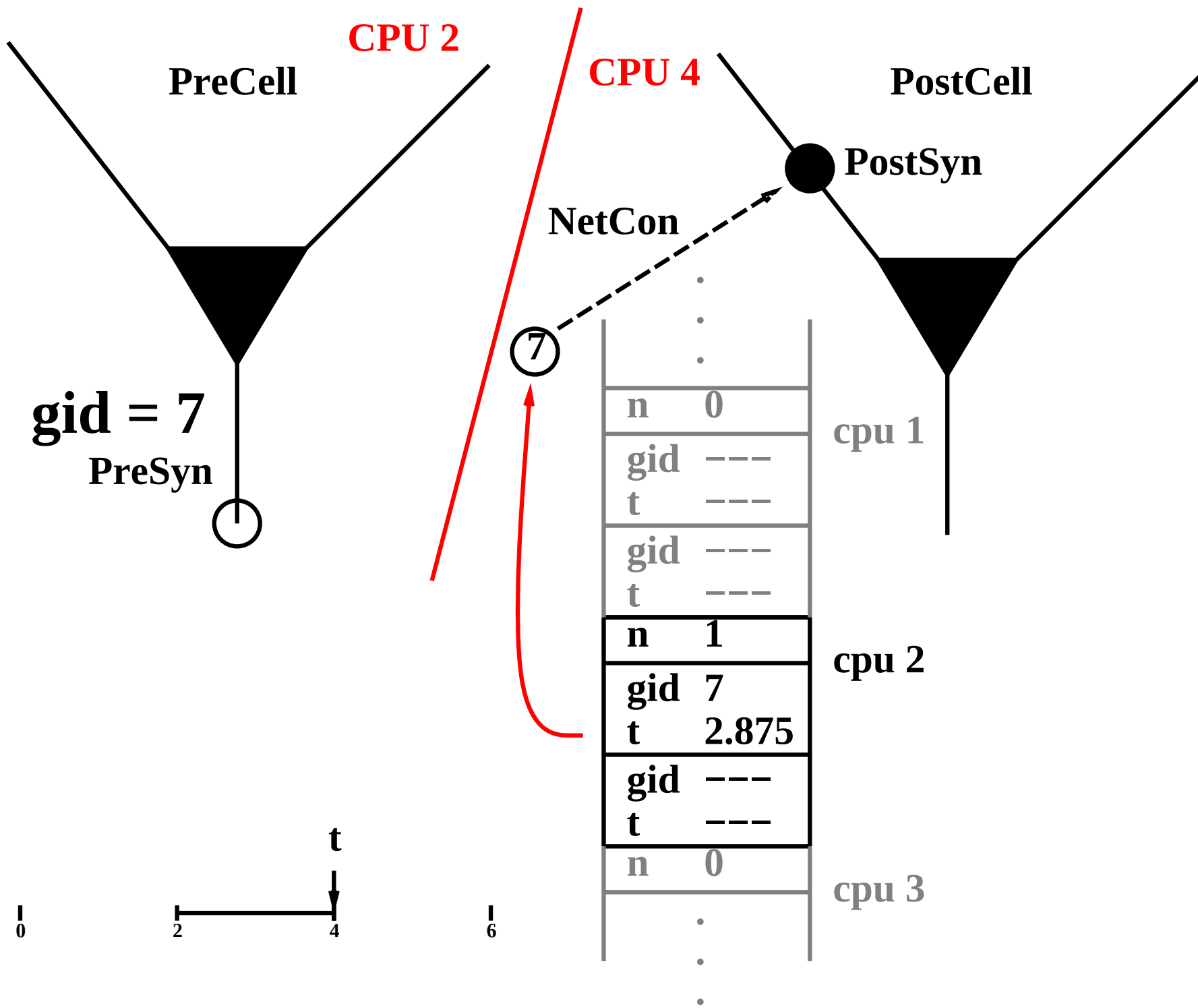
gid = 7
PreSyn

7



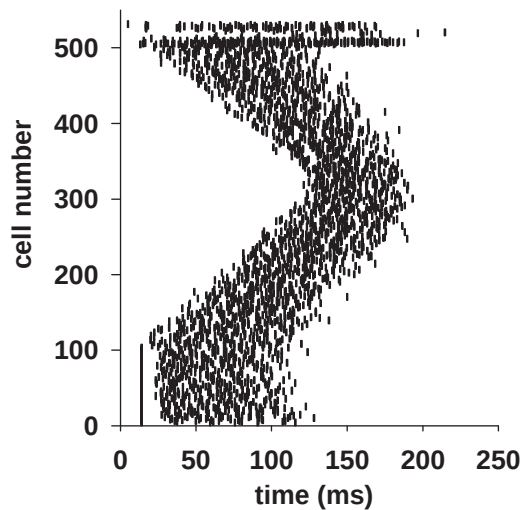




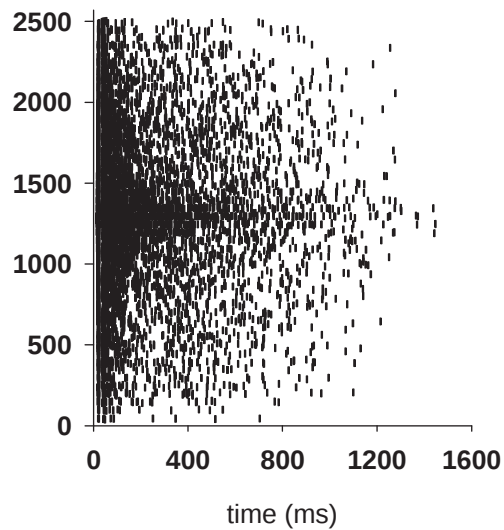


Migliore et al (2006) J. Comput. Neurosci. 21(2):119

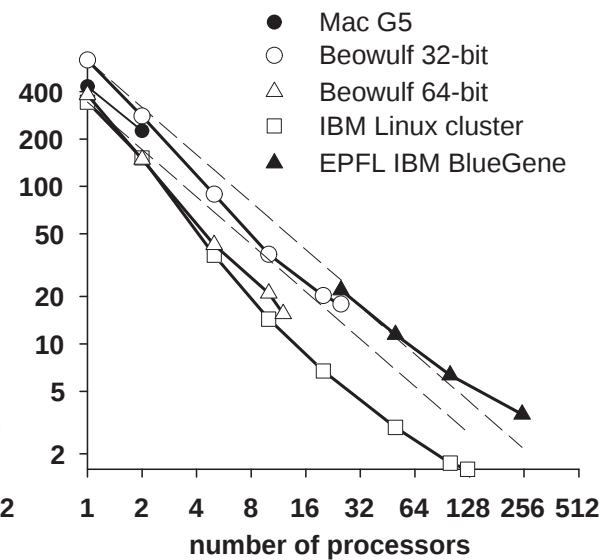
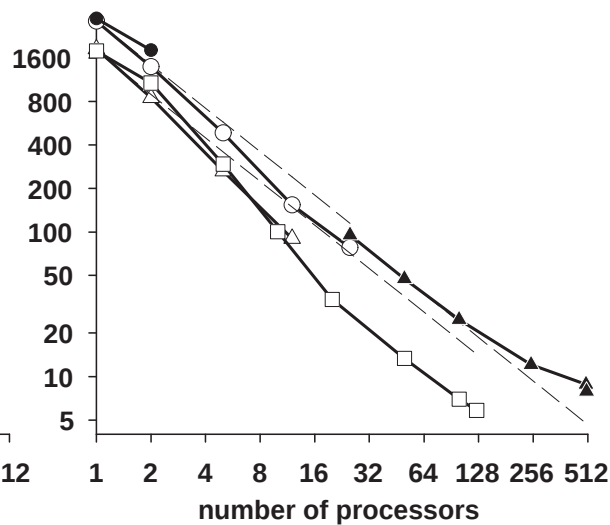
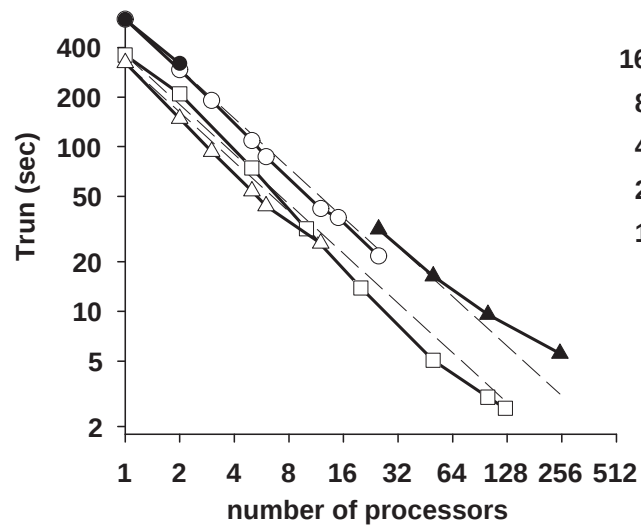
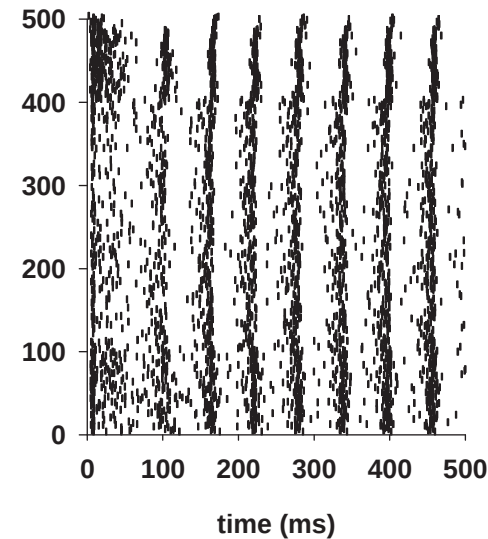
Santhakumar et al. (2005)



Davison et al., (2003)

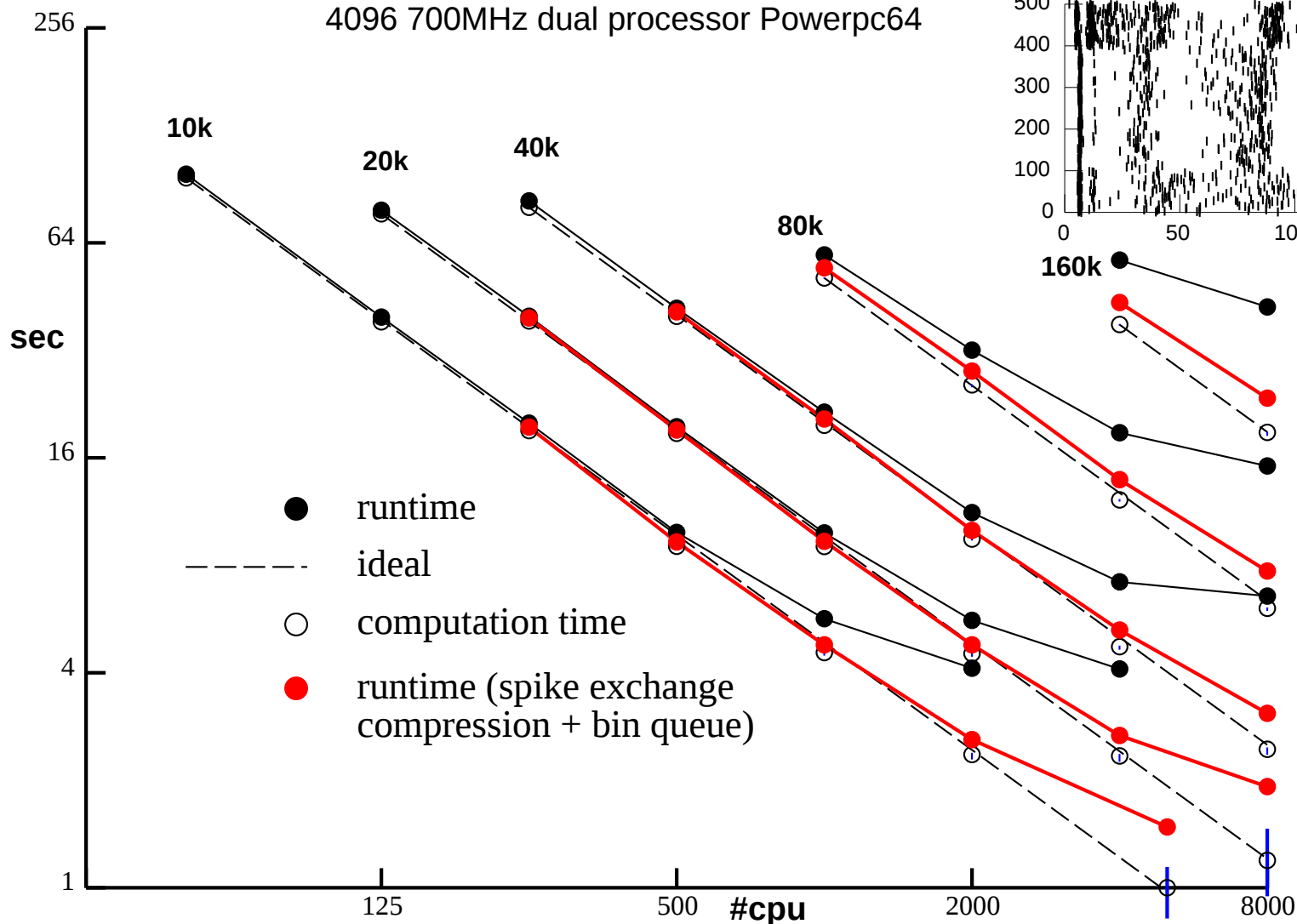


Bush et al., (1999)



EPFL IBM BlueGene/L

4096 700MHz dual processor Powerpc64



#cells	#states	#netcons	#outputSpikes	#spikedeliver
10k	444,664	17,167,785	31,118	52,040,794
20k	888,664	68,655,566	33,960	110,634,002
40k	1,777,664	274,591,128	69,529	452,987,907
80k	3,553,664	1,098,302,267	294,974	2,147,483,647 *
160k	7,107,664	4,393,084,577	844,175	22,847,784,937

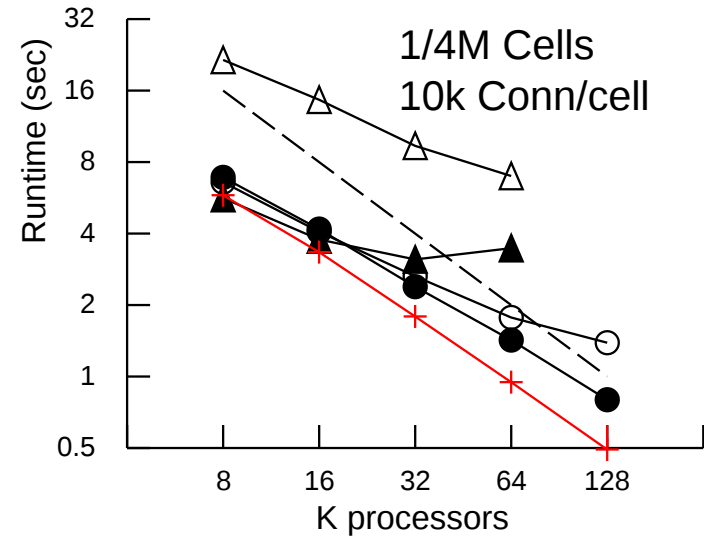
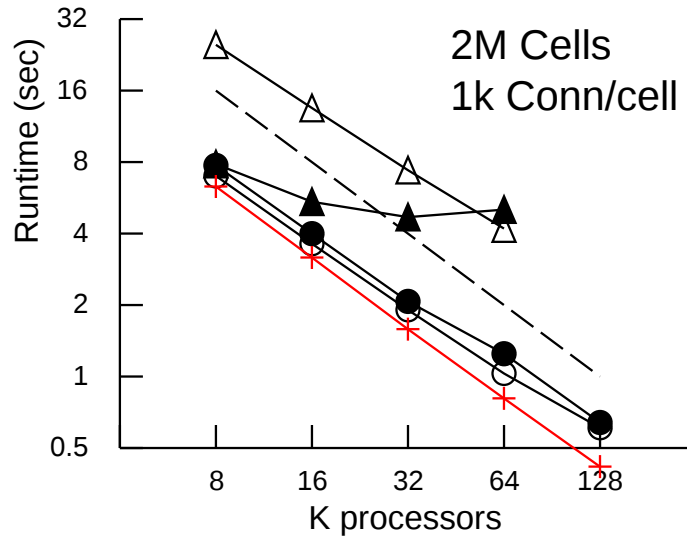
Artificial Spiking Net

Blue Gene/P

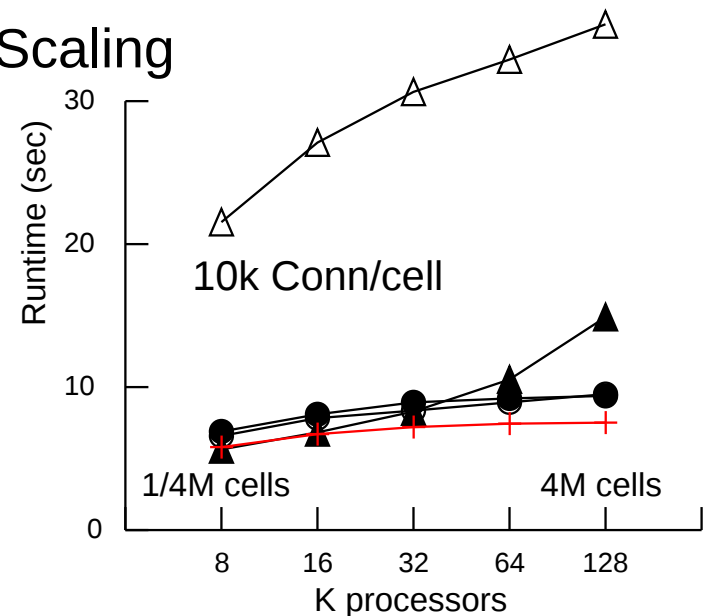
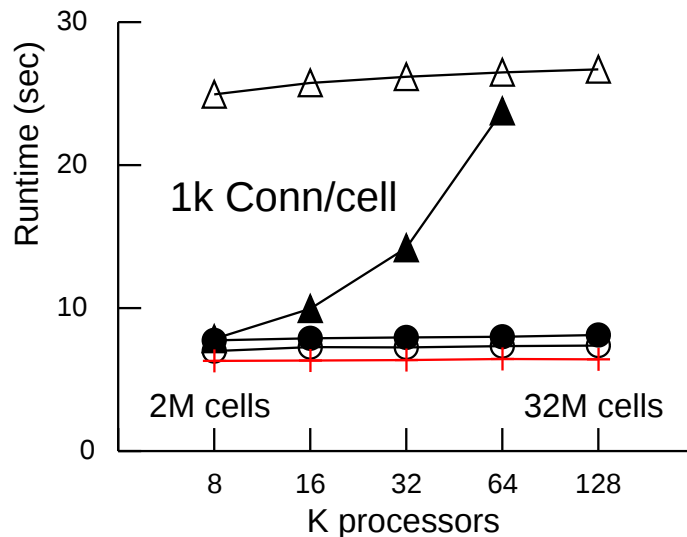
Argonne National Lab

- △ MPI_ISend – Two Phase, Two Subinterval
- ▲ Allgather
- DCMF_Multicast – Two Phase, Two Subinterval
- Record-Replay – One Subinterval
- + Computation Time (includes queue)

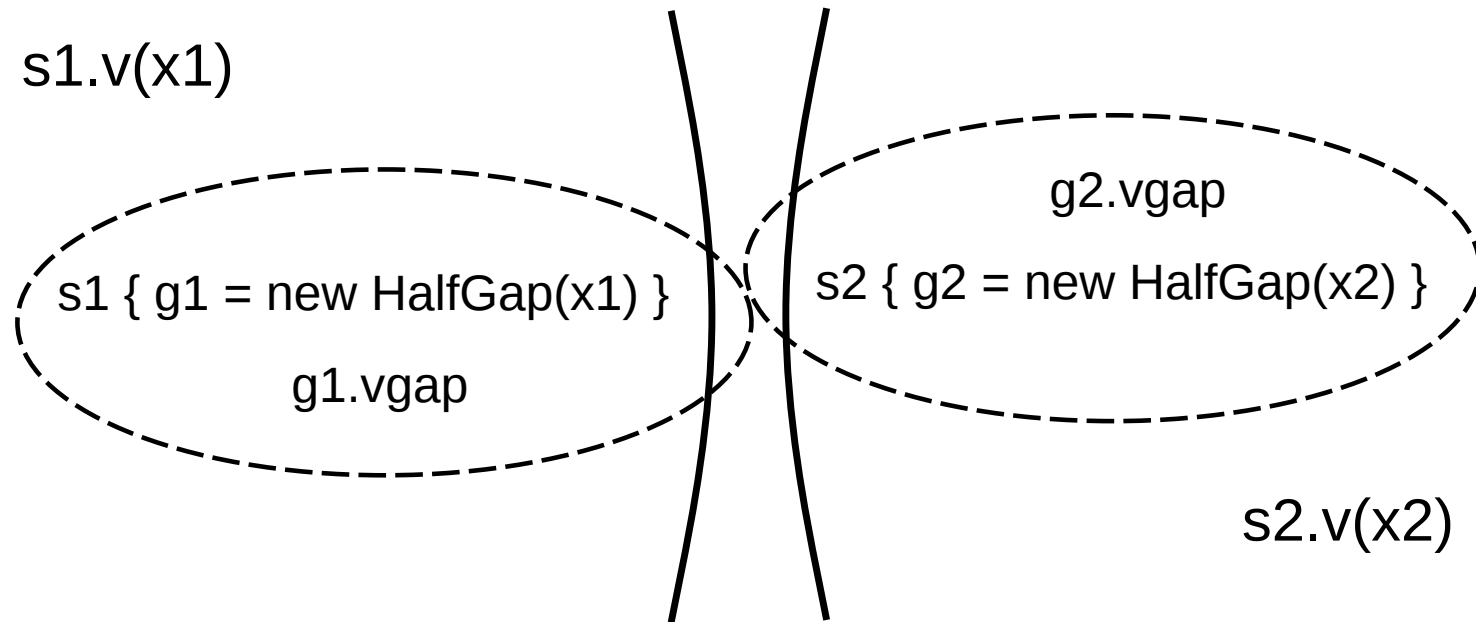
Strong Scaling



Weak Scaling



Continuous Voltage Exchange



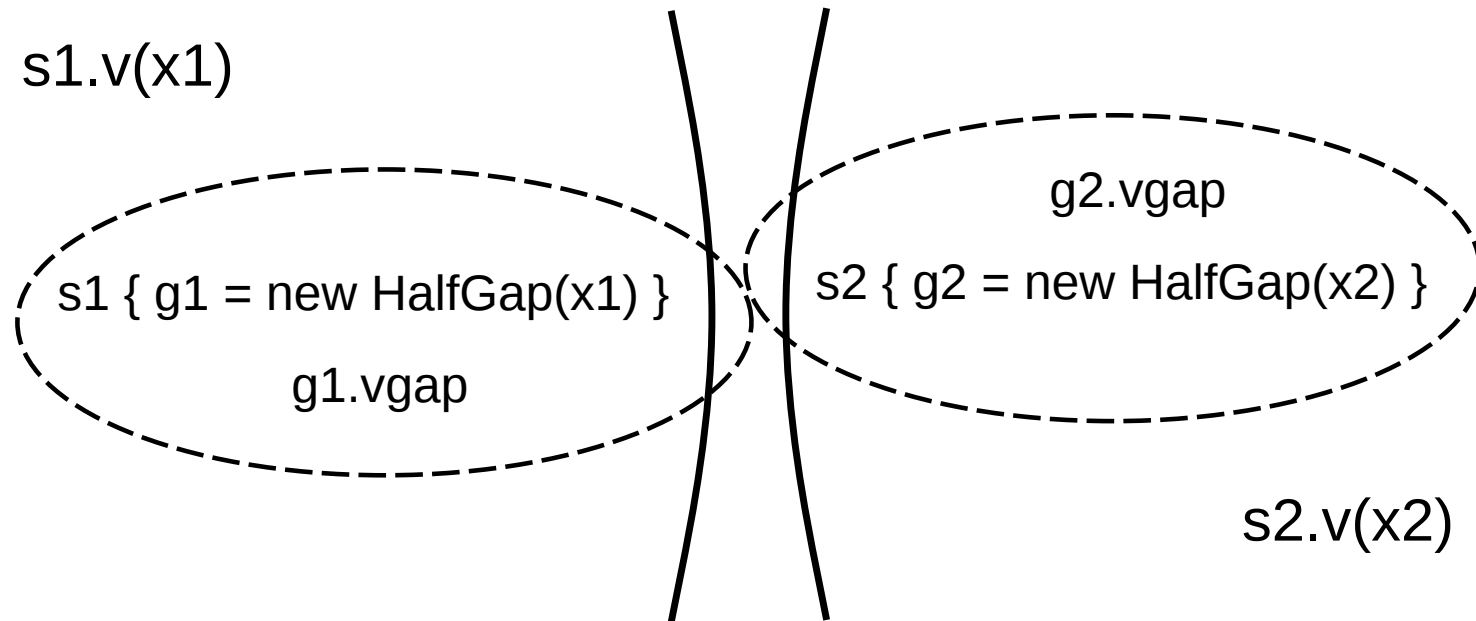
gap.mod

```
NEURON {  
  POINT_PROCESS HalfGap  
  ELECTRODE_CURRENT i  
  RANGE r, i, vgap  
}  
PARAMETER { r = 1e9 (megohm) }
```

```
ASSIGNED {  
  v (millivolt)  
  vgap (millivolt)  
  i (nanoamp)  
}  
CURRENT { i = (vgap - v)/r }
```

Continuous Voltage Exchange

`pc.source_var(&source_var, sgid)`



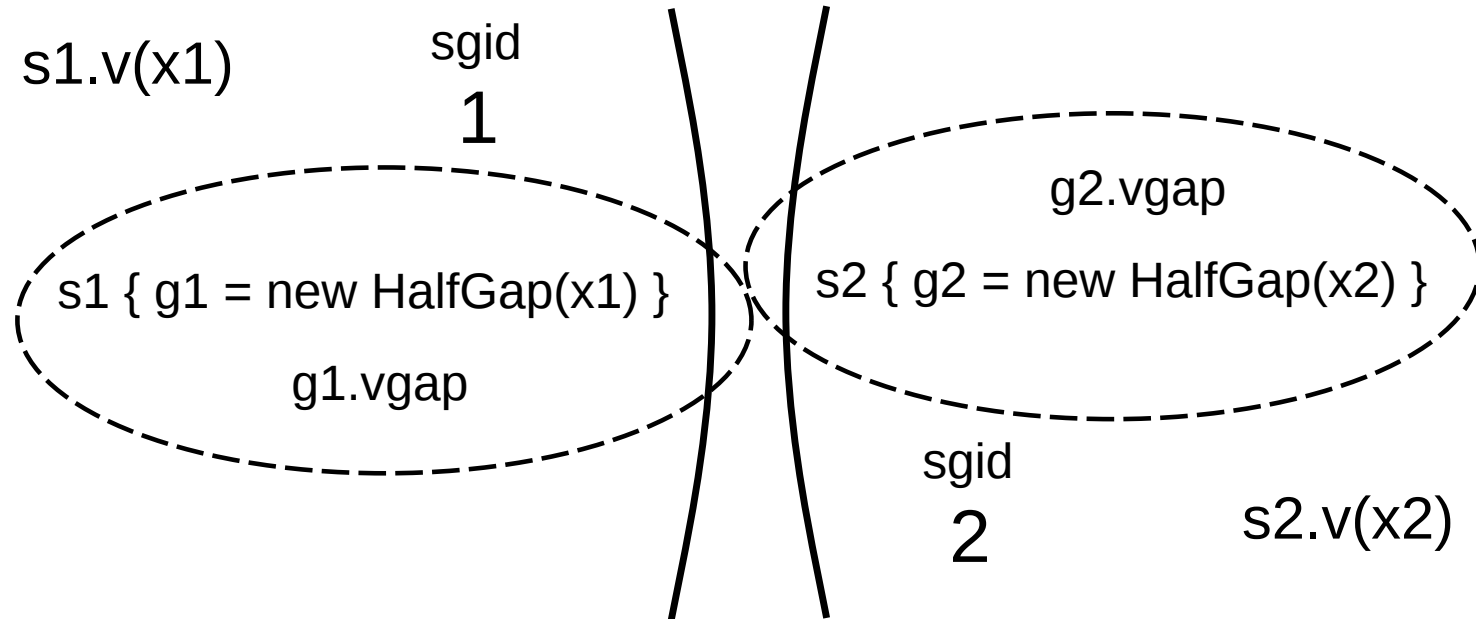
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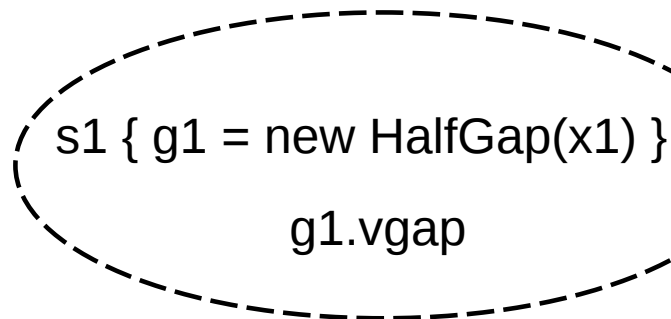
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  vgap (millivolt)  
  i (nanoamp)  
}  
CURRENT { i = (vgap - v)/r }
```

Continuous Voltage Exchange

`pc.source_var(&source_var, sgid)`

`pc.source_var(&s1.v(x1), 1)`

`s1.v(x1)` ↔ `sgid`
1



`g2.vgap`
`s2 { g2 = new HalfGap(x2) }`

`sgid`
2 ↔ `s2.v(x2)`

`pc.source_var(&s2.v(x2), 2)`

`gap.mod`

```
NEURON {  
  POINT_PROCESS HalfGap  
  ELECTRODE_CURRENT i  
  RANGE r, i, vgap  
}  
PARAMETER { r = 1e9 (megohm) }
```

```
ASSIGNED {  
  v (millivolt)  
  vgap (millivolt)  
  i (nanoamp)  
}  
CURRENT { i = (vgap - v)/r }
```

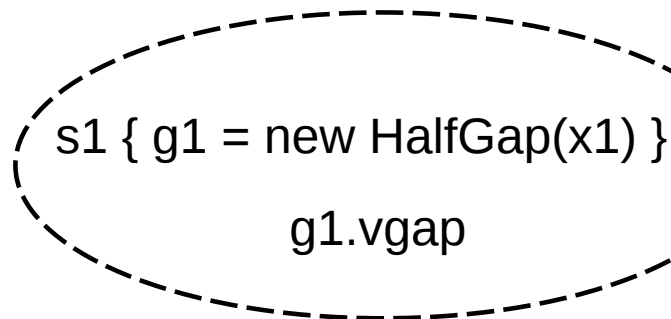
Continuous Voltage Exchange

`pc.source_var(&source_var, sgid)`

`pc.target_var(&target_var, sgid)`

`pc.source_var(&s1.v(x1), 1)`

`s1.v(x1)` $\longleftrightarrow$ `sgid`
1



`g2.vgap`
`s2 { g2 = new HalfGap(x2) }`

`sgid`
2 $\longleftrightarrow$ `s2.v(x2)`

`pc.source_var(&s2.v(x2), 2)`

gap.mod

```
NEURON {
  POINT_PROCESS HalfGap
  ELECTRODE_CURRENT i
  RANGE r, i, vgap
}
```

```
PARAMETER { r = 1e9 (megohm) }
```

```
ASSIGNED {
  v (millivolt)
  vgap (millivolt)
  i (nanoamp)
}
```

```
CURRENT { i = (vgap - v)/r }
```

Continuous Voltage Exchange

pc.source_var(&source_var, sgid)

pc.target_var(&target_var, sgid)

pc.source_var(&s1.v(x1), 1)

s1.v(x1) ↔ sgid
1

pc.target_var(&g2.vgap, 1)

g2.vgap

s1 { g1 = new HalfGap(x1) }

s2 { g2 = new HalfGap(x2) }

g1.vgap

pc.target_var(&g1.vgap, 2)

sgid

2 ↔ s2.v(x2)

pc.source_var(&s2.v(x2), 2)

gap.mod

```
NEURON {
  POINT_PROCESS HalfGap
  ELECTRODE_CURRENT i
  RANGE r, i, vgap
}
PARAMETER { r = 1e9 (megohm) }
```

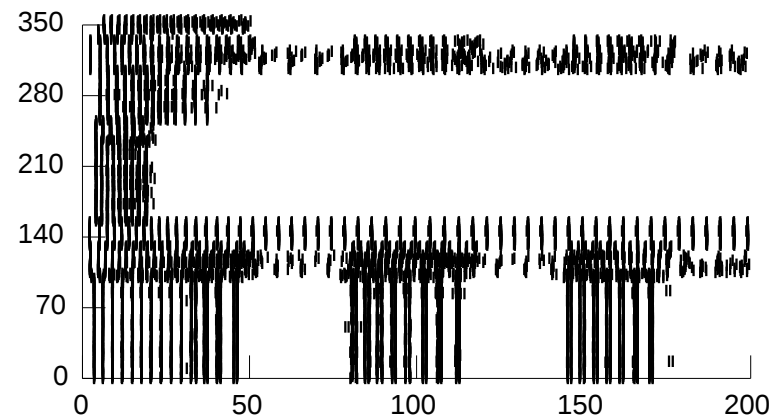
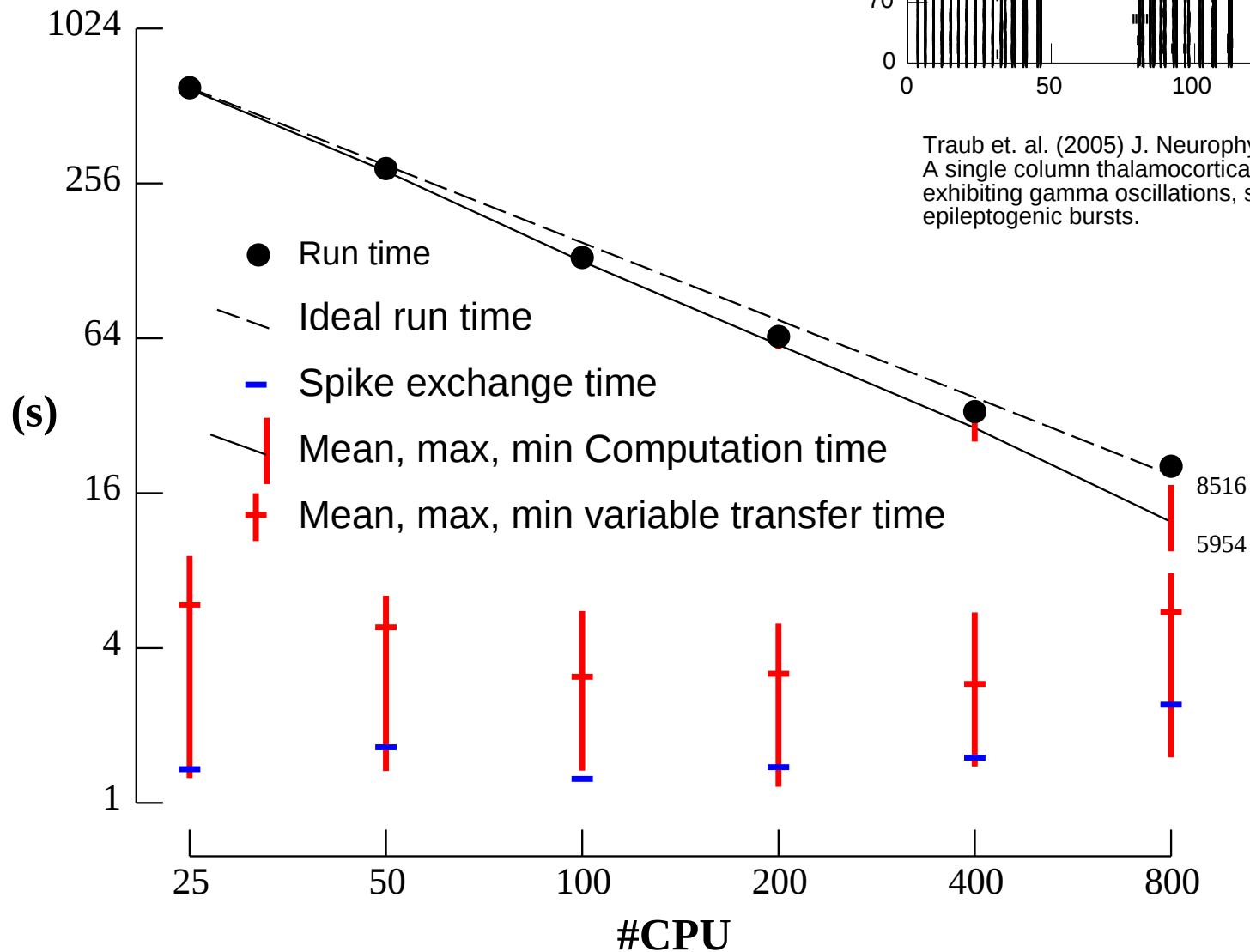
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  vgap (millivolt)
  i (nanoamp)
}
CURRENT { i = (vgap - v)/r }
```

Pittsburgh Supercomputing Center

Bigben

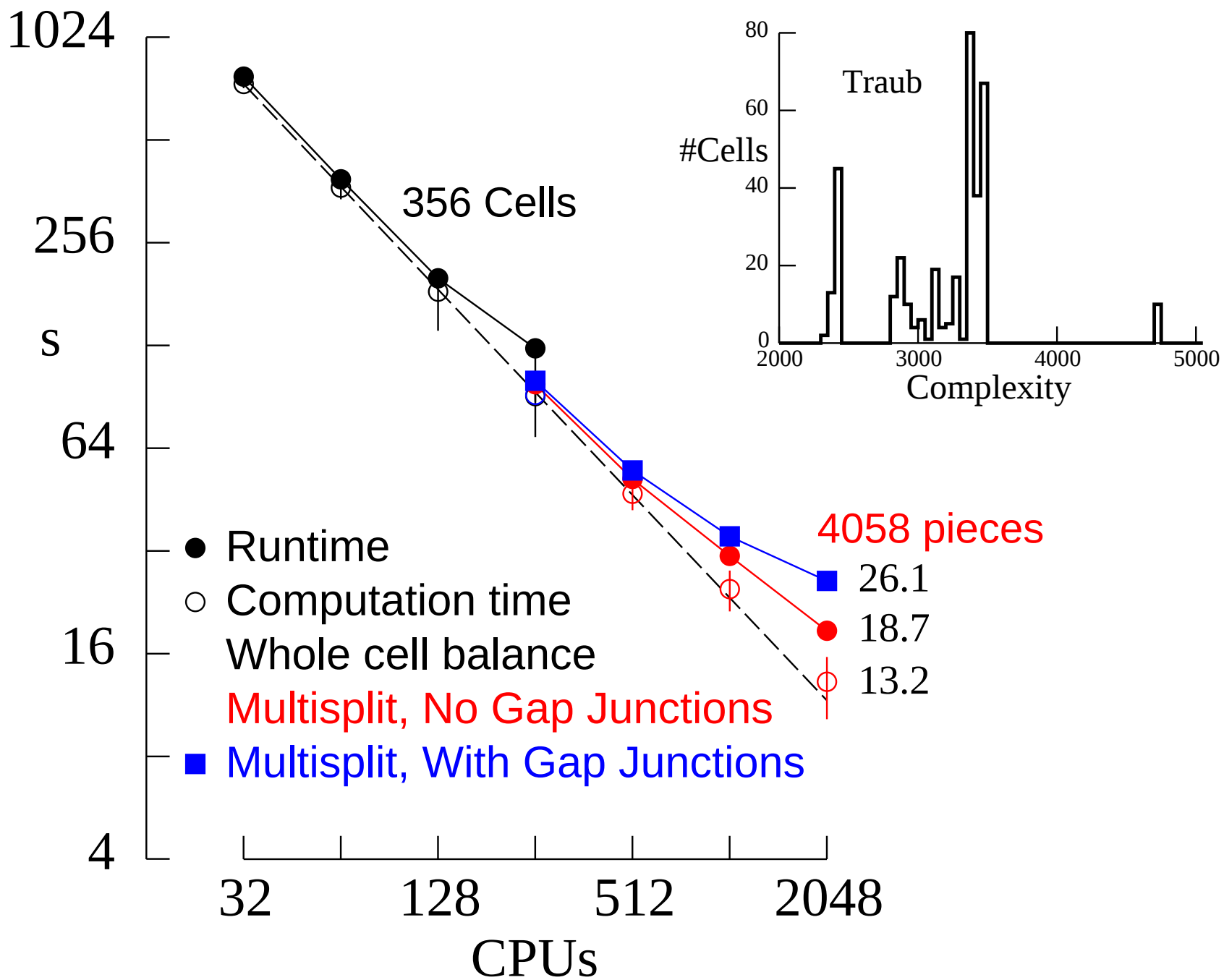
Cray XT3

2068 2.4 GHz Opteron Processors

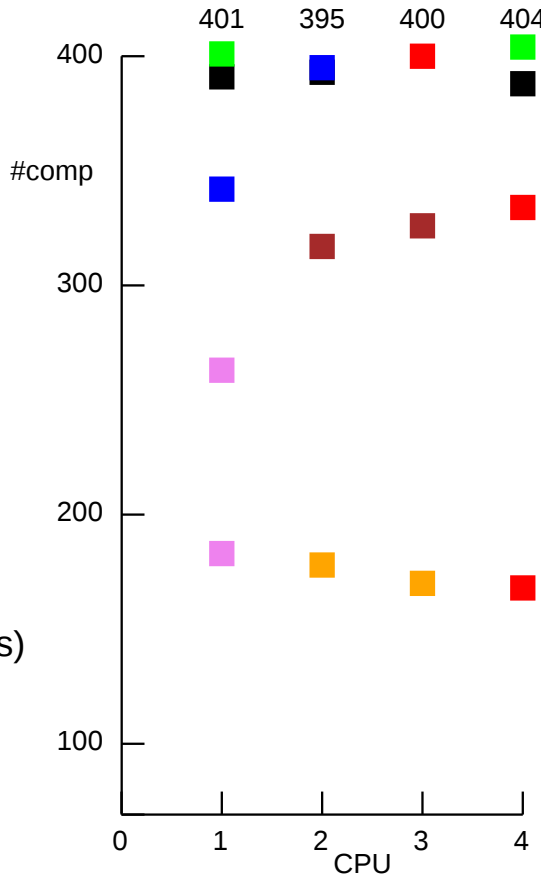
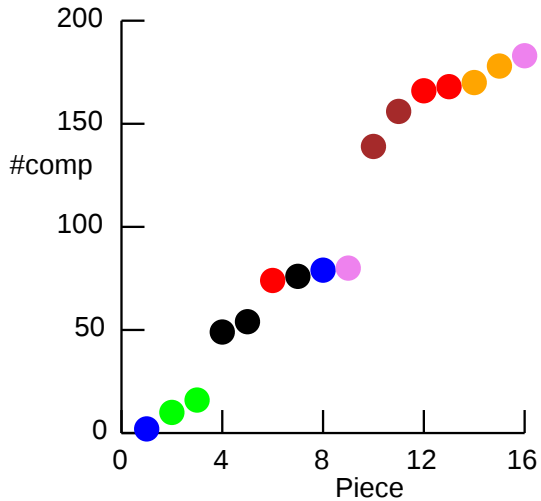
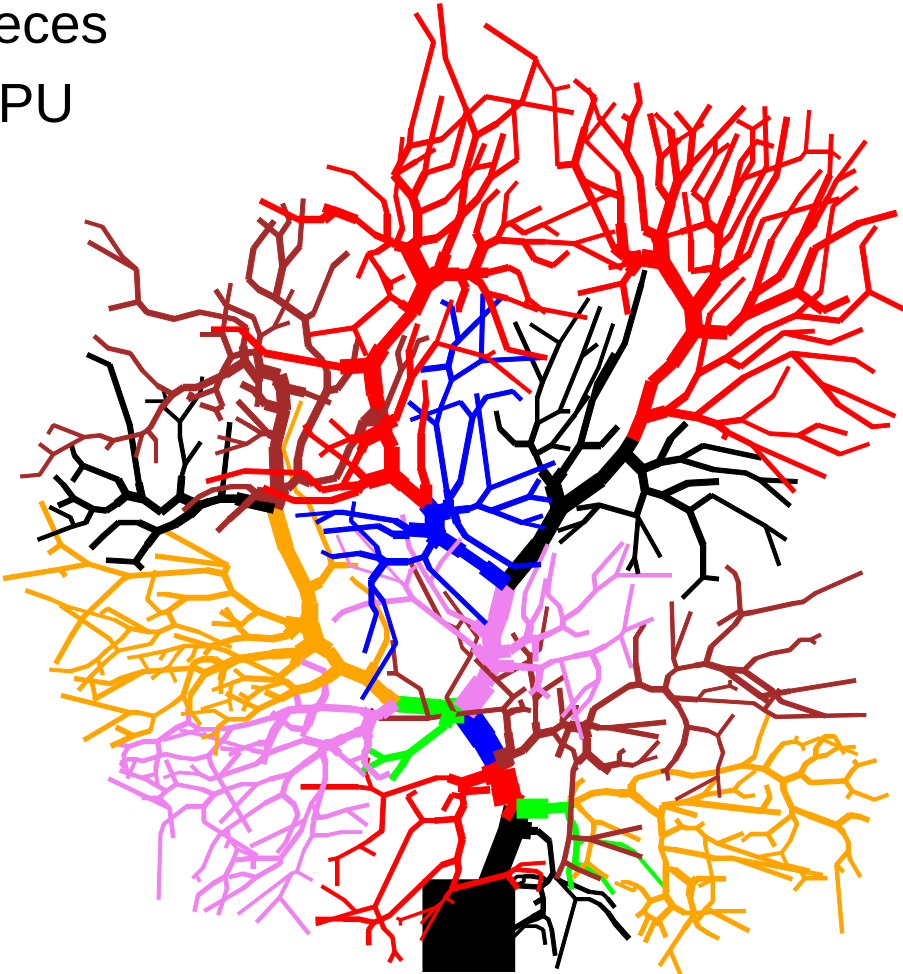


Traub et. al. (2005) J. Neurophysiol 93: 2194
A single column thalamocortical network model
exhibiting gamma oscillations, sleep spindles and
epileptogenic bursts.

3560 cells 14 types
3500 gap junctions
5,596,810 equations
73,465 spikes
1,122,520 connections
19,844,187 delivered



16 Pieces
4 CPU



Time (s)			
CPU	Computation	Exchange	
0	13.82	0.56	
1	13.35	1.03	16 pieces, 1 cpu
2	13.47	0.90	wholecell, 1 cpu
3	13.56	0.82	16 pieces, 4 cpu

Runtime(s)	
16 pieces, 1 cpu	55.0
wholecell, 1 cpu	56.2
16 pieces, 4 cpu	14.4

